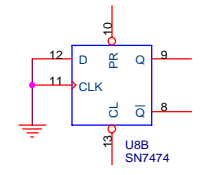
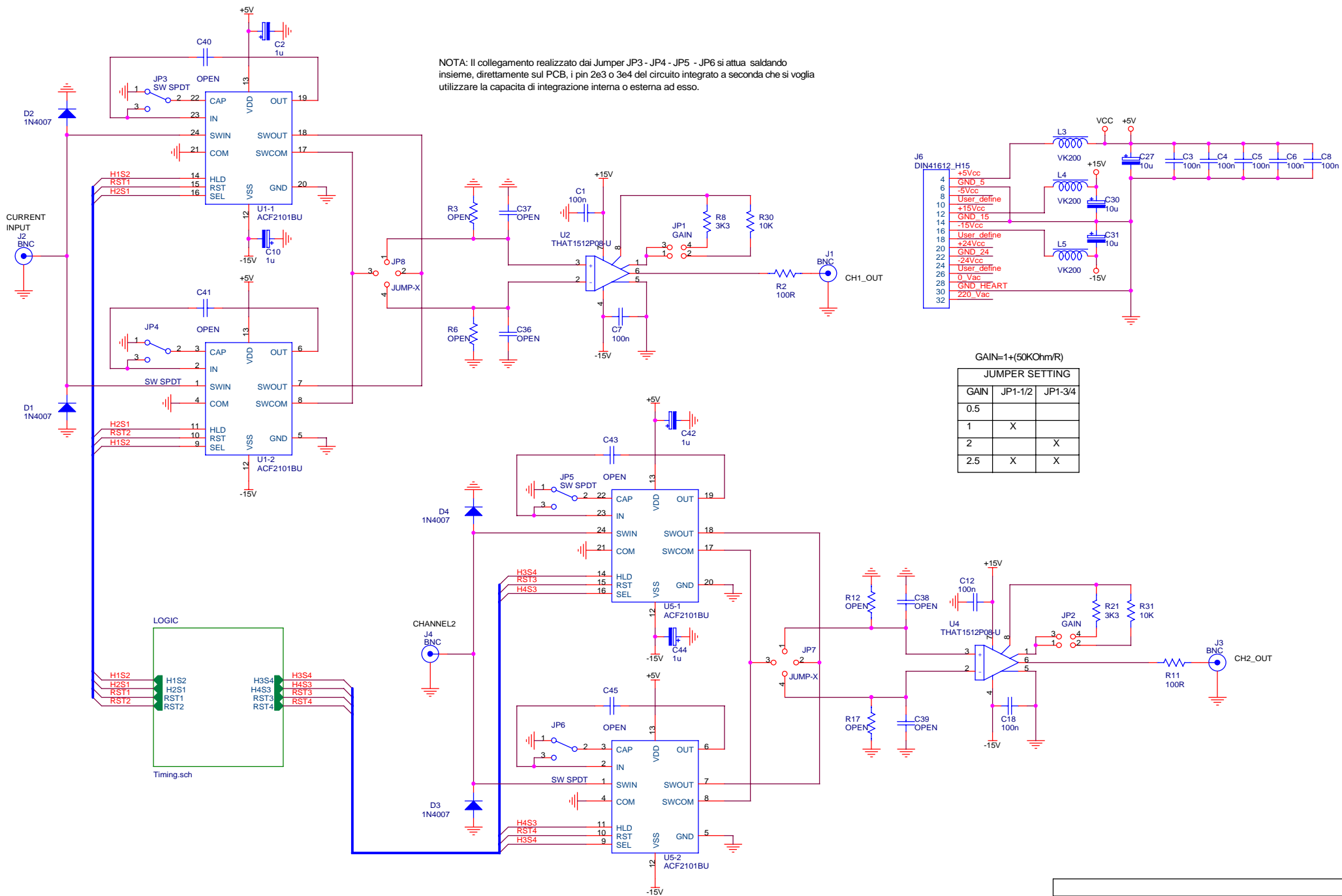


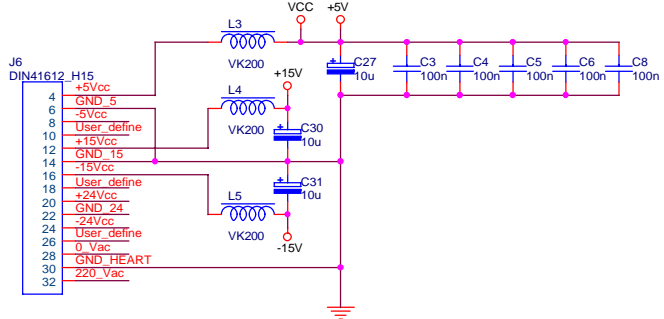
NOTA: Nella Rev.01 sono stati scambiati fra di loro gli integrati U9A - U9B e U6A - U6B e i rispettivi collegamenti.



Title		
Dual Channel Gated Integrator		
Size	Document Number	Rev
A3	DUAL_CHANNEL_INTEGRATOR/Timing.sch	02
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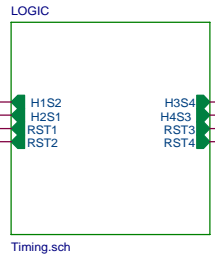


NOTA: Il collegamento realizzato dai Jumper JP3 - JP4 - JP5 - JP6 si attua saldando insieme, direttamente sul PCB, i pin 2e3 o 3e4 del circuito integrato a seconda che si voglia utilizzare la capacita di integrazione interna o esterna ad esso.



GAIN=1+(50KOhm/R)

JUMPER SETTING		
GAIN	JP1-1/2	JP1-3/4
0.5		
1	X	
2		X
2.5	X	X



Dual Channel Gated Integrator Revised: Friday, March 09, 2012
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Item	Q.ty	Reference	Description	Value	Supplier Ref
1	9	C1, C3, C4, C5, C6, C7, C8, C12, C18	Cap. Cer. 50v p.5.08	100n	
2	4	C2, C10, C42, C44	Cap. Tant. 25v SMT3528	1u	DigyKey 399-5073-1-ND
3	5	C9, C36, C37, C38, C39	Cap. Cer. 50v p.5.08	OPEN	
4	4	C21, C22, C28, C46	Cap. Cer. 50v p.5.08	1n	
5	2	C26, C25	Cap. Cer. 50v p.5.08	680p	
6	3	C27, C30, C31	Cap. Tant. 25v p.2.54	10u	
7	4	C40, C41, C43, C45	Cap. Poly. 50v p.5.08	OPEN	
8	4	D1, D2, D3, D4	Diode	1N4007	
9	2	JP1, JP2	Jumper	GAIN	
10	4	JP3, JP4, JP5, JP6	Jumper	SW SPDT	
11	2	JP7, JP8	Jumper	JUMP-X	
12	4	J1, J2, J3, J4	Connector PCB	BNC	RS 512-1225
13	1	J6	Connector PCB	DIN41612_H15	Farnell 1096871
14	2	J8, J7	Connector	BNC Radiall	R141.557.000
15	3	L3, L4, L5	Induttanza	VK200	
16	1	Q1	Transistor	2N2222	
17	3	R1, R30, R31	Res, 1/4W 1%	10K	
18	4	R2, R11, R24, R25	Res, 1/4W 1%	100R	
19	4	R3, R6, R12, R17	Res, 1/4W 1%	OPEN	
20	2	R21, R8	Res, 1/4W 1%	3K3	
21	2	R23, R13	Trimmer	10K	
22	2	R32, R15	Res, 1/4W 1%	8K2	
23	2	R19, R18	Res, 1/4W 1%	13K	
24	1	R20	Res, 1/4W 1%	1K	
25	2	U1, U5	Integrated Circuits	ACF2101BU	Farnell 1754679
26	2	U2, U4	Integrated Circuits	THAT1512P08-U	Farnell 1354162
27	3	U6, U7, U9	Integrated Circuits	74HC221	
28	2	U11, U8	Integrated Circuits	SN7474	